Ultracompact 160 Gbaud all-optical demultiplexing exploiting slow light in an engineered silicon photonic crystal waveguide

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We demonstrate all-optical demultiplexing of a high-bandwidth, time-division multiplexed 160 Gbit/s signal to 10 Gbit/s channels, exploiting slow light enhanced four-wave mixing in a dispersion engineered, $96\,\mu\mathrm{m}$ long planar photonic crystal waveguide. We report error-free (bit error rate< 10^{-9}) operation of all 16 demultiplexed channels, with a power penalty of 2.2–2.4 dB, highlighting the potential of these structures as a platform for ultracompact all-optical nonlinear processes. © 2011 Optical Society of America

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The manipulation of subpicosecond length optical pulses, of fundamental interest for monitoring or controlling fast interactions and for potential application in terabaud serial communication links [1], requires response times for devices faster than those achievable with current electronics (i.e., >100 GHz). Ultrafast optical nonlinearities, which can respond on femtosecond time scales [2], provide a useful mechanism to handle these short pulses all-optically. One key functionality required to handle signals running at baud rates >100 Gbaud is the ability to switch out low-baud-rate channels from a high-baud-rate data stream, i.e., demultiplexing of timedivision multiplexed data. There have been many successful demonstrations of time-division demultiplexing of signals exceeding 100 Gbaud, utilizing hundreds of meters of specialty fiber [3,4] or centimeter length scale waveguides in periodically poled lithium niobate [5], semiconductor optical amplifier [6], chalcogenide [7], or silicon/Si-hybrid [8-10] materials. Further increasing waveguide nonlinearity allows for a reduction in the length of such devices and would provide a path to dense integration of all-optical signal processing devices on a single photonic chip. The key to increasing the waveguide nonlinearity is maximizing the energy density within waveguides. A first step toward this is to utilize high-index contrast structures that are able to provide submicrometer confinement of light. To increase energy density further, resonant structures can be used, such as ring resonators [11] or photonic crystal (PhC) cavities [12]. However, demonstrations utilizing these structures for all-optical switching have typically been limited in bandwidth from a few gigahertz to at most a few tens of gigahertz. Slow light in PhC waveguides provides a semiresonant method for enhancing nonlinear interactions through increased energy density with distributed feedback [13]. In contrast to resonant cavities, dispersion engineered PhC waveguides [14] have provided demonstrations of enhanced nonlinearities over almost a terahertz of bandwidth [15]. This has established the

potential for these types of waveguides to provide low-footprint, high-bandwidth all-optical devices.

Here we present $160\,\mathrm{Gb/s}$ to $10\,\mathrm{Gb/s}$ optical timedivision demultiplexing exploiting slow light enhanced nonlinearity in a $96\,\mu\mathrm{m}$ long waveguide. To the best of our knowledge, this is the shortest device successfully used for demultiplexing of data streams above $100\,\mathrm{GBaud}$, demonstrating the potential of dispersion engineered PhC waveguides in providing dense integration of all-optical elements on a chip-based platform.

Our demultiplexing scheme switches out $10\,\mathrm{Gb/s}$ channels from a $160\,\mathrm{Gb/s}$ data stream via four-wave mixing (FWM; Fig. 1) [9]. A $10\,\mathrm{GHz}$ pulse train (at $1553.5\,\mathrm{nm}$), synchronized to the data stream (at $1547.5\,\mathrm{nm}$), converts a $10\,\mathrm{Gb/s}$ channel in the $160\,\mathrm{Gb/s}$ optical time-division multiplexed (OTDM) data stream to the FWM idler wavelength (around $1559.5\,\mathrm{nm}$). This demultiplexed channel can then be optically filtered to separate it from the $160\,\mathrm{Gbit/s}$ data stream.

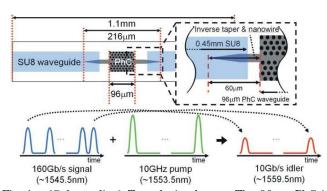


Fig. 1. (Color online) Top: device layout. The $96\,\mu m$ PhC is connected to a $10\,\mu m$ long nanowire and $50\,\mu m$ long inverse taper. Light is coupled to the silicon waveguide segment via a polymer rib waveguide. Bottom: schematic of FWM demultiplexing. When the signal and pump temporally overlap, the signal is reproduced at the FWM idler wavelength.

Slow light in the $96\,\mu\mathrm{m}$ long PhC waveguide is crucial to achieving this functionality in our compact waveguide, as this enhances the FWM interaction. In the PhC waveguide, the FWM conversion efficiency is enhanced proportional to the product of the group indices of the four waves [16], i.e., scaling with $n_{g\,\mathrm{pump}}^2 \times n_{g\,\mathrm{signal}} \times n_{g\,\mathrm{idler}}$. If all four waves involved travel at similar group velocities, the conversion efficiency of FWM is enhanced as S^4 (the slow down factor [17–19] $S = n_g/n_{\mathrm{eff}} \sim 10 \pm 5\%$ from 1545 to 1561 nm). This enhancement can be used to lower the overall length of the waveguide needed to observe significant FWM [18,19].

The device used in this demonstration (Fig. 1) incorporates a 96 µm long dispersion engineered PhC waveguide [14]: an air-suspended silicon structure 220 nm thick, with a lattice period of 404 nm, hole radii of ~230 nm, and row shift parameters [14] $s_1 = -50 \,\mathrm{nm}$ and $s_2 =$ 0 nm (similar to that used to investigate FWM in [18]). It has a low dispersion region (see Fig. 3) with a group velocity of $v_q \sim c/30$ over a 16 nm bandwidth from 1545 to 1561 nm, with a modal effective area $(A_{\rm eff} \sim 0.5 \,\mu{\rm m}^2)$. Coupling to the slow mode is enhanced by a mode conversion interface [20], and recently experiments have shown that similar dispersion engineered PhC waveguide designs provide record low propagation losses in the slow light regime [21]. The PhC waveguide is connected on both sides to 10 µm long access nanowires $(220 \,\mathrm{nm} \times 700 \,\mathrm{nm} \,\mathrm{cross} \,\mathrm{section})$ with $50 \,\mu\mathrm{m} \,\mathrm{long}$ inverse tapers. Large mode area $(A_{\rm eff} \sim 8 \,\mu{\rm m}^2)$ polymer waveguides complete the structure at both chip ends [19] to improve coupling to the device via butt-coupled lensed fibers (Fig. 1). The total fiber-to-fiber loss when probing the slow light band was ~14 dB, including losses (coupling and propagation) over the silicon waveguide segment of $\sim 3 \, dB$.

The signal is generated from a 40 GHz optical pulse train (CLK), on-off keyed with a 2³¹ - 1 pattern length pseudorandom bit sequence (PRBS) at 40 Gb/s, then time interleaved to provide a 20% duty cycle, 160 Gb/s signal. The pump is a 10 GHz, 1.9% duty cycle pulse train. Both pump and signal are amplified, filtered, and combined immediately before to the silicon chip. The signal and pump are copolarized and coupled to the TE-like mode of the PhC waveguide. Dispersive broadening of the signal is minimized with a spectral phase filter (Finisar WaveShaper). Temporal alignment of the signal and pump pulses is ensured by maximizing the FWM idler at the output of the waveguide, controlled by a variable optical delay line. We estimate coupled average powers in the PhC waveguide of ~17 mW and ~14 mW for the pump and signal, respectively, with pump pulse energy at the beginning of the PhC waveguide ~2.5 pJ. The demultiplexed channel at the idler frequency is passed through a 2 nm filter (centered ~1559.5 nm) before being sent to the receiver assembly (Fig. 2).

On chip, the FWM interaction used for demultiplexing occurs primarily in the $96\,\mu\text{m}$ long section of slow light PhC waveguide. The short length of the nanowire waveguides (see [18]) and the large mode area of the polymer waveguides limits FWM in those sections. By comparing the signal and idler powers, derived by integrating appropriate sections of the measured spectrum at the output of the waveguide [Fig. 3(b)], we infer an FWM conver-

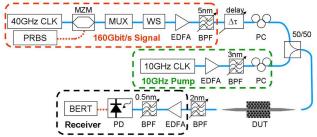


Fig. 2. (Color online) FWM demux experiment. A 40 GHz pulse train is modulated (MZM) and time interleaved (MUX) to provide a 160 Gb/s signal and copropagated with a 10 GHz pulse train in the device under test (DUT) (WS—spectral phase filter, EDFA—erbium fiber amplifier, BPF—bandpass filter, PC—polarization controller, PD—photodetector, BERT—bit error rate test).

sion efficiency (CE = $P_{\text{idler out}}/P_{\text{signal out}}$) of ~ - 33 dB $(\sim -18 \, dB \text{ instantaneous CE}, \text{ when taking into account})$ a factor of 1/16 for difference in repetition rate and 1/2 for the 50/50 data duty cycle PRBS). To quantify the enhancement of FWM afforded by slow light in our sample, we compare the measured value of instantaneous CE with an approximate calculated CE for an ideal PhC without the influence of slow light. Assuming lossless and dispersionless propagation, for a mode far from the band edge (typically $A_{\rm eff} \sim 0.2 \, \mu {\rm m}^2$, $n_g \sim 6$), the CE can be calculated as $(\gamma P_{\rm peak \; pump} L)^2 = ([2\pi n_2]/[24 \, {\rm gg}])^2 / [24 \, {\rm g}]$ (23 dB) lower than the measured CE. This difference in CE is primarily due to the different group index, effective area (CE scales as S^4 and as $1/A_{\text{eff}}^2$ [16,18]), and loss in the two cases compared and is lower than a simple factor of $S^4 \sim 10^4$. We expect phase matching in these short waveguides to have a minor effect on CE [18]. Eye diagrams of initial 160 Gb/s signal and demultiplexed 10 Gb/s idler are measured [Figs. 4(a) and 4(b)], the idler clearly showing a single 10 Gb/s channel with negligible residual traces of the other channels from the 160 Gb/s data stream. By tuning the variable optical delay, all 16 10 Gb/s channels were demultiplexed and bit error rate (BER) curves measured.

The small spread in the demultiplexed channel BER curves indicates that the 160 Gbit/s signal was well multiplexed. The curves for the demultiplexed signals can be compared to the "back-to-back" case, where the pulses

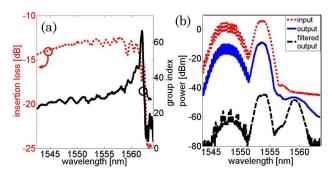


Fig. 3. (Color online) (a) Device insertion loss and PhC TE mode group index, (b) spectra captured by optical spectrum analyzer, pump $\sim\!1553.5\,\mathrm{nm}$, signal $\sim\!1547.5\,\mathrm{nm}$, generated idler $\sim\!1559.5\,\mathrm{nm}$. The filtered output spectrum is after a $2\,\mathrm{nm}$ filter centered on the idler.

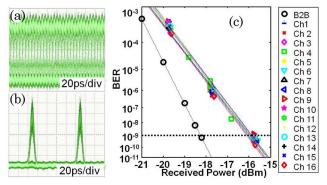


Fig. 4. (Color online) (a) $160\,\mathrm{Gb/s}$ signal and (b) $10\,\mathrm{Gb/s}$ idler eye diagrams from a $65\,\mathrm{GHz}$ oscilloscope; (c) BER curves for all $16\,10\,\mathrm{Gb/s}$ channels. "B2B" represents the back-to-back measurement.

from the 10 GHz optical clock (previously used as the pump, now tuned to the idler wavelength) are modulated in the same manner as the signal, then bypass the PhC device to directly connect to the receiver assembly. Comparing the back-to-back BER curve with the demultiplexed curves, a power penalty of 2.2-2.4 dB is observed at the "error-free" (BER $<10^{-9})$ level. This penalty from our micrometer-scale device compares well with a recent similar demultiplexing experiment (~3.9 dB penalty) in a 1.1 cm long silicon nanowire waveguide [9]. It should be noted that the CE of our device was limited by nonlinear loss, which is significant in silicon waveguides. We would expect improved CE at lower pump powers by using longer PhC waveguides engineered for low linear propagation loss [21], which would lead to more efficient demultiplexing. Dispersion engineered structures in materials such as optimized III-V semiconductor blends [22] or chalcogenide glasses [23] may also provide more efficient device operation.

In conclusion, we have successfully demonstrated error-free demultiplexing of 160 Gb/s data in an ultracompact silicon PhC waveguide device. As the 96 μ m long PhC waveguide is the main nonlinear waveguide element on the chip, this shows the potential of such structures to play a key role in achieving dense integration of alloptical devices on a single photonic chip.

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